

Hutchip  
 HUTCHIP

**HC3139KE**

**20V P-Channel MOSFET**

### General Description

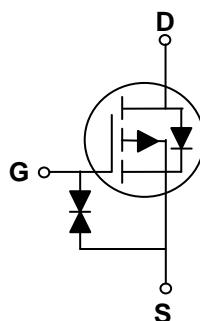
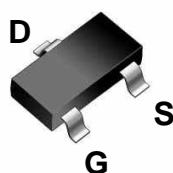
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### Features

$V_{DS}$	-20V
$I_D$ (at $V_{GS}=-4.5V$ )	-0.5A
$R_{DS(ON)}$ (at $V_{GS}=-4.5V$ )	580mΩ(Typ)

ESD Protected Up to 2.0KV (HBM)

SOT523



### Absolute Maximum Ratings $T_A=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	$V_{DS}$	-20	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Drain Current-Continuous	$I_D$ (TC=25°C)	-0.5	A
	$I_D$ (TC=100°C)	-0.4	A
Maximum Power Dissipation	$P_D$	0.18	W
Drain Current – Pulsed1	$I_{DM}$	-2.6	A
Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	°C
Thermal Characteristics			
Parameter	Symbol	Typ	Max
Thermal Resistance junction-to-solder point	$R_{\theta Jsp}$		40
Thermal Resistance junction-to-Ambient	$R_{\theta JA}$		690

## Electrical Characteristics (TJ=25°C unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
<b>STATIC PARAMETERS</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-20			V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=-20V, V_{GS}=0V$			1	$\mu A$
$I_{GSS}$	Gate-Body Leakage Current	$V_{GS}=\pm 10V, V_{DS}=0V$			$\pm 10$	$\mu A$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.3	-0.6	-1.2	V
$R_{DS(ON)}$	Drain-Source On-State Resistance	$V_{GS}=-4.5V, I_D=-0.5A$		580	850	$m\Omega$
		$V_{GS}=-2.5V, I_D=-0.3A$		850	1200	$m\Omega$
		$V_{GS}=-1.8V, I_D=-0.2A$		1350	2000	$m\Omega$
$I_S$	Maximum Body-Diode Continuous Current				-0.5	A
<b>DYNAMIC PARAMETERS</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=-10V, V_{GS}=0V, F=1.0MHz$		71		pF
$C_{oss}$	Output Capacitance			20		pF
$C_{rss}$	Reverse Transfer Capacitance			15		pF
<b>SWITCHING PARAMETERS</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}=-10V, I_D=-0.2A, V_{GS}=-4.5V, R_G=10\Omega$		4		nS
$t_r$	Turn-on Rise Time			19		nS
$t_{d(off)}$	Turn-Off Delay Time			16		nS
$t_f$	Turn-Off Fall Time			25		nS
$Q_g$	Total Gate Charge	$V_{DS}=-10V, I_D=-0.2A, V_{GS}=-4.5V$		1.2		nC
$Q_{gs}$	Gate-Source Charge			0.37		nC
$Q_{gd}$	Gate-Drain Charge			0.27		nC
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0V, I_{SD}=-1A$		0.7	1.2	V

Note:

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width  $\leq 300\mu s$  , duty cycle  $\leq 2\%$ .
3. Essentially independent of operating temperature.

## TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

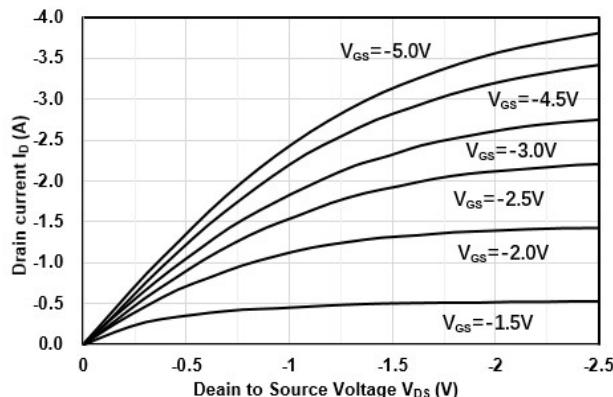


Figure1. Output Characteristics

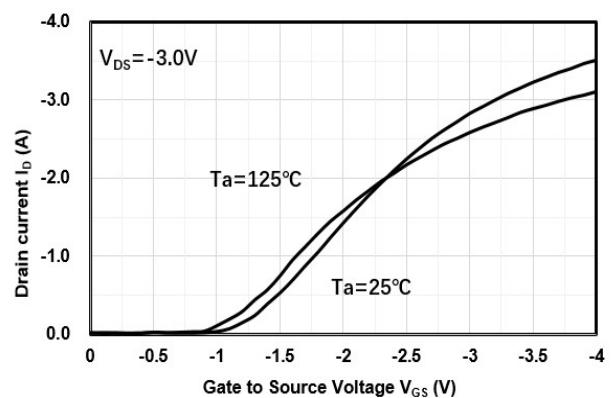


Figure2. Transfer Characteristics

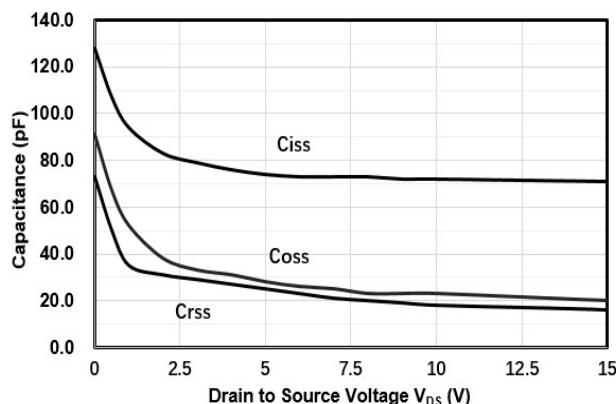


Figure3. Capacitance Characteristics

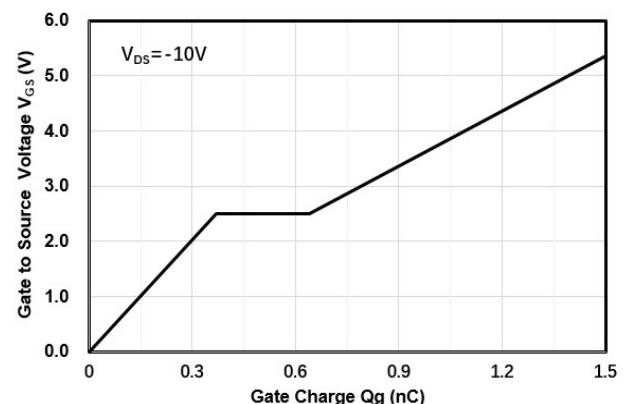


Figure4. Gate Charge

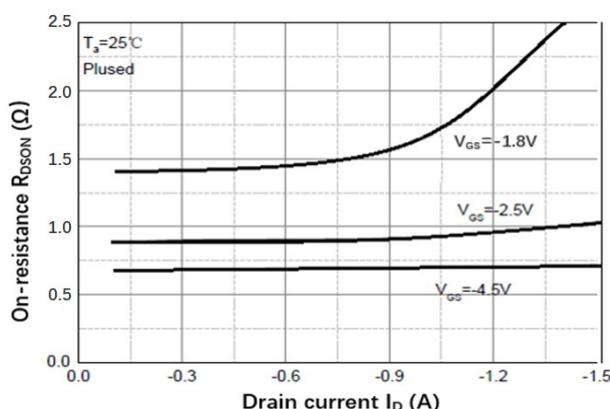


Figure5. Drain-Source on Resistance

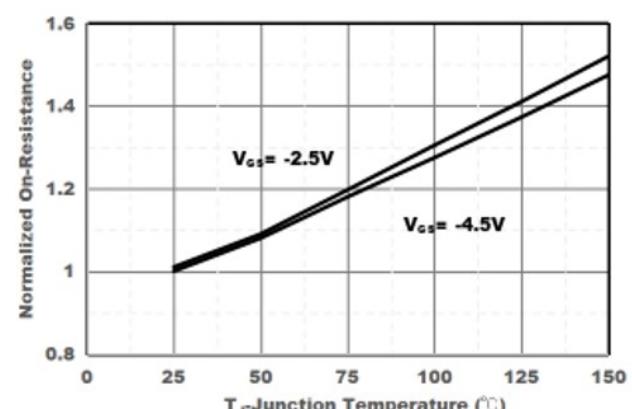


Figure6. Drain-Source on Resistance

## TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

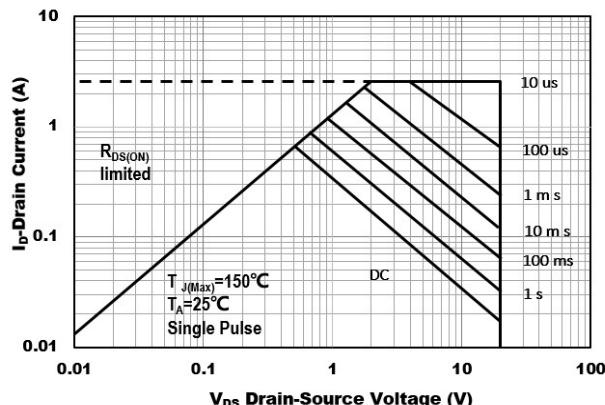


Figure 7. Safe Operation Area

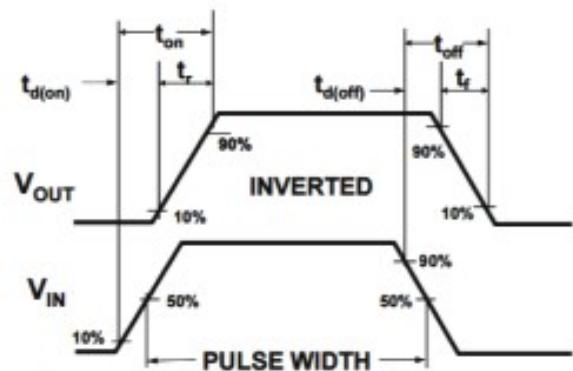
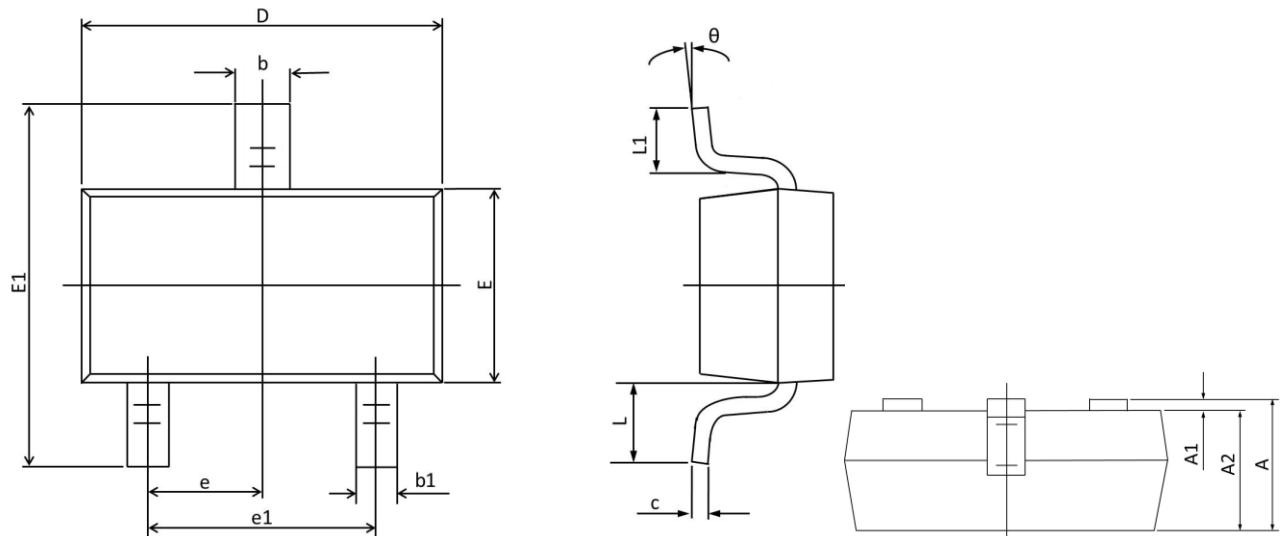


Figure 8. Switching wave

## SOT523 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	0.900	0.700	0.035	0.028
A1	0.100	0.000	0.004	0.000
A2	0.800	0.700	0.031	0.028
b	0.350	0.250	0.014	0.010
b1	0.250	0.150	0.010	0.006
c	0.200	0.100	0.008	0.004
D	1.750	1.500	0.069	0.059
E	0.900	0.700	0.035	0.028
E1	1.750	1.400	0.069	0.055
e	0.5TYP.		0.02TYP.	
e1		0.900	0.043	0.035
L	0.460	0.300	0.018	0.012
L1	0.460	0.260	0.018	0.010
θ	8°	0°	8°	0°