HC4435A

30V P-Channel MOSFET

General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

Features

V_{DS}	-30V
I_D (at V_{GS}=-10V)	-10A
$R_{DS(ON)}$ (at V_{GS}=-10V)	15mΩ(Typ)
$R_{DS(ON)}$ (at V_{GS}=-4.5V)	20mΩ(Typ)

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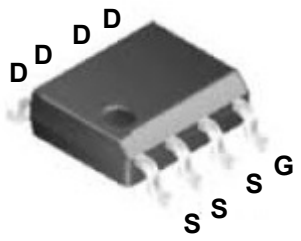
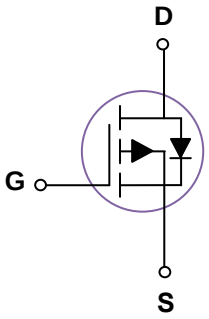




Absolute Maximum Ratings T_A=25°C unless otherwise noted

Parameter		Symbol	Maximum	Units
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	±20	V
Drain Current-Continuous	TC=25°C	I_D	-10	A
	TC=100°C	I_D	-7.6	A
Drain Current – Pulsed		I_{DM}	-48	A
Maximum Power Dissipation		P_D	2.5	W
Junction and Storage Temperature Range		T_J,T_{STG}	-55 To 150	℃

Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance junction-case	$R_{\theta Jc}$		24	℃ /W
Thermal Resistance junction-to-Ambient	$R_{\theta JA}$		60	℃ /W

Electrical Characteristics (TJ=25℃ unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V I_D=-250μA	-30			V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=-30V,V_{GS}=0V			1	μA
I_{GSS}	Gate-Body Leakage Current	V_{GS}=±20V,V_{DS}=0V			±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS},I_D=-250μA	-1.0	-1.5	-2.5	V
$R_{DS(ON)}$	Drain-Source On-State Resistance	V_{GS}=-10V, I_D=-8.0A		15	21	mΩ
		V_{GS}=-4.5V, I_D=-5.0A		20	30	mΩ
DYNAMIC PARAMETERS						
C_{iss}	Input Capacitance	V_{DS}=-15V,V_{GS}=0V, F=1.0MHz		1630		pF
C_{oss}	Output Capacitance			180		pF
C_{rss}	Reverse Transfer Capacitance			125		pF
SWITCHING PARAMETERS						
$t_{d(on)}$	Turn-on Delay Time	V_{DS}=-15V,I_D=-1A, V_{GS}=-10V, R_G=6Ω		9		nS
t_r	Turn-on Rise Time			21		nS
$t_{d(off)}$	Turn-Off Delay Time			59		nS
t_f	Turn-Off Fall Time			14		nS
Q_g	Total Gate Charge	V_{DS}=-15V,I_D=-8A, V_{GS}=-4.5V		14		nC
Q_{gs}	Gate-Source Charge			4.1		nC
Q_{gd}	Gate-Drain Charge			6.3		nC
V_{SD}	Diode Forward Voltage	V_{GS}=0V,I_{SD}=-1A		0.72	1.4	V

Note：

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%.
3. Essentially independent of operating temperature.

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

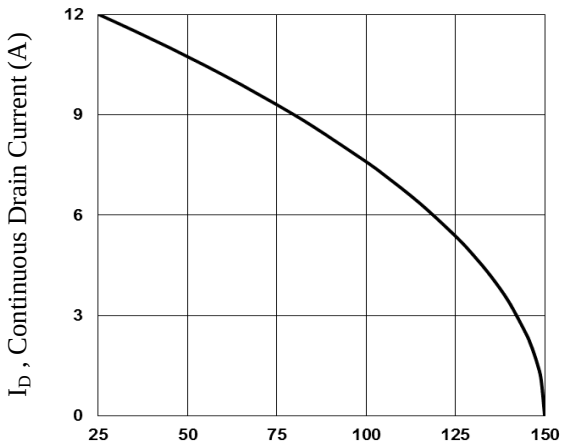


Fig.1 Continuous Drain Current vs. T_C

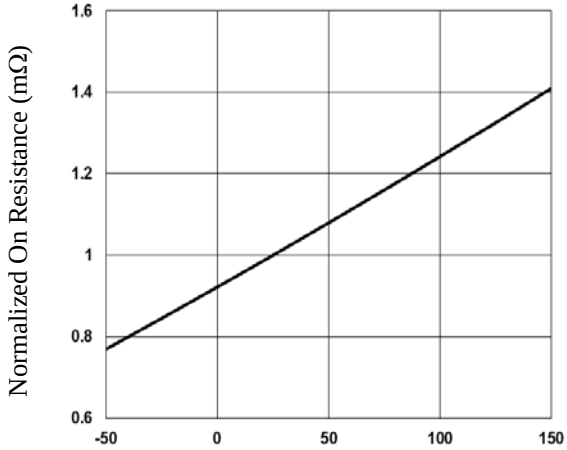


Fig.2 Normalized RDSON vs. T_J

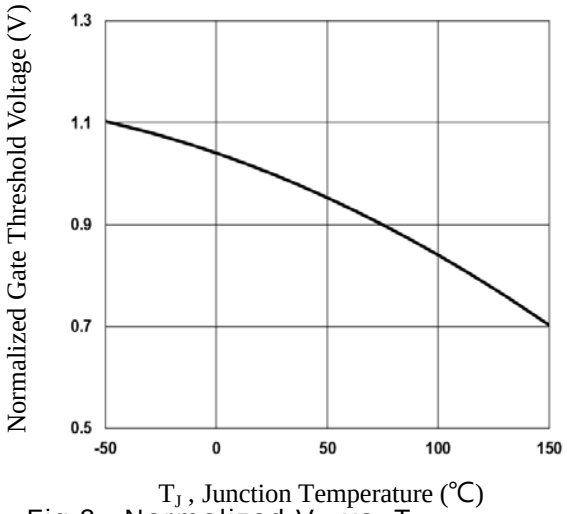


Fig.3 Normalized V_{th} vs. T_J

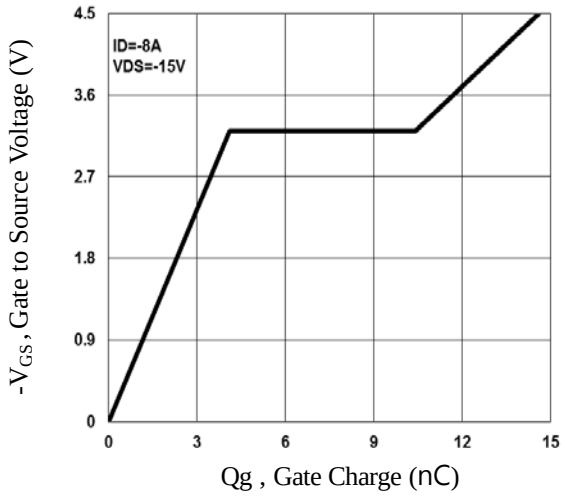


Fig.4 Gate Charge Waveform

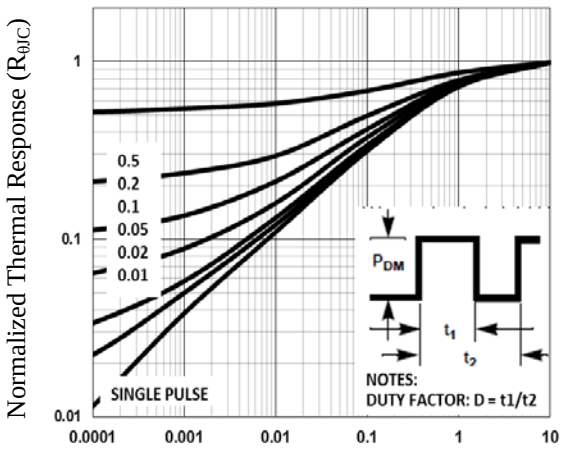


Fig.5 Normalized Transient Impedance

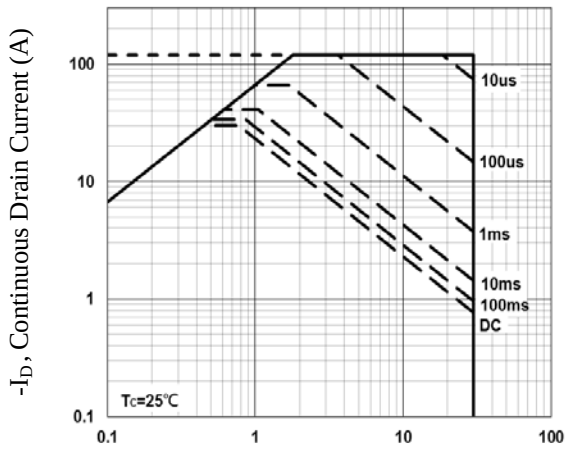


Fig.6 Maximum Safe Operation Area

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

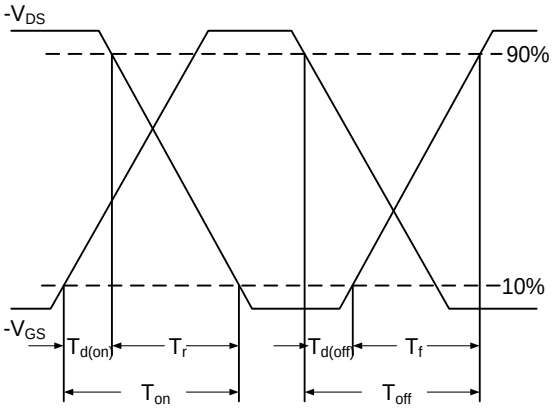


Fig.7 Switching Time Waveform

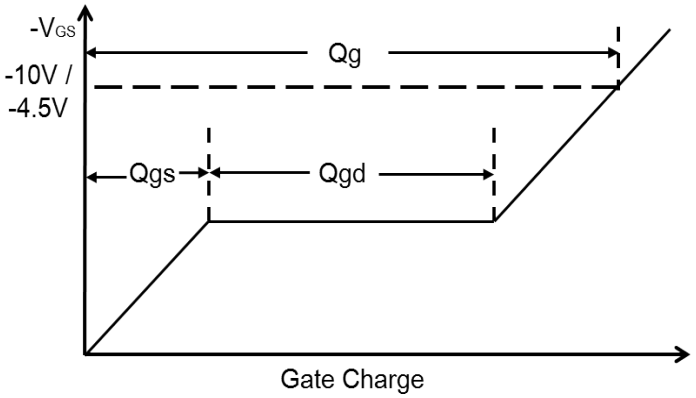
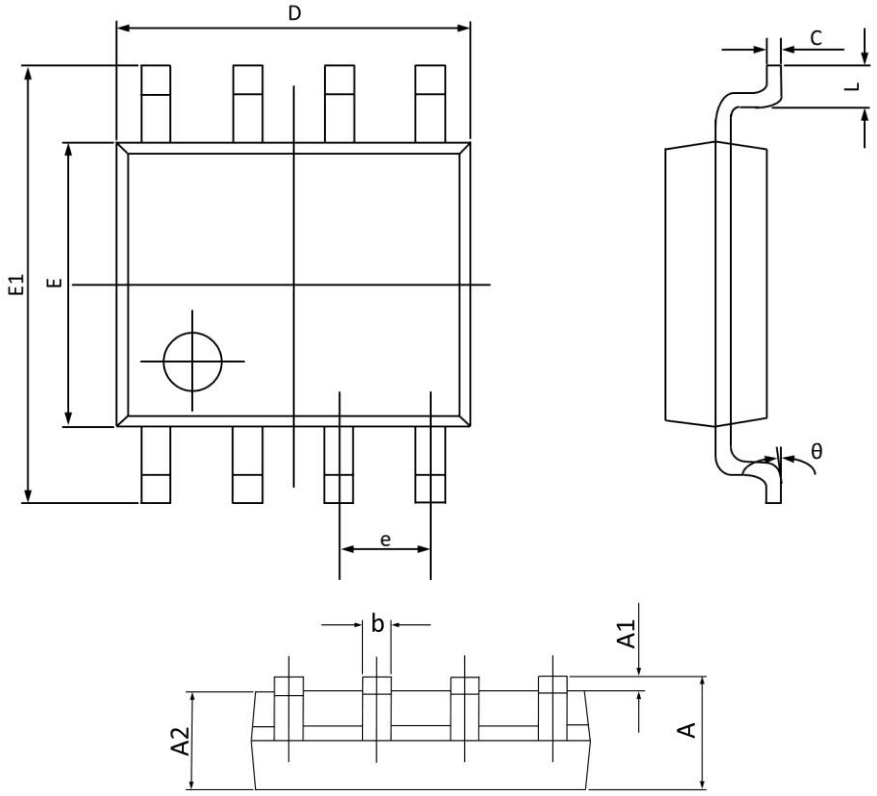


Fig.8 Gate Charge Waveform

SOP8 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	1.750	1.350	0.069	0.053
A1	0.250	0.100	0.010	0.004
A2	1.500	1.300	0.059	0.051
b	0.490	0.350	0.019	0.014
C	0.260	0.190	0.010	0.007
D	5.100	4.700	0.201	0.185
E	4.100	3.700	0.161	0.146
E1	6.200	5.800	0.244	0.228
e	1.27BSC		0.05BSC	
L	0.900	0.400	0.035	0.016
θ	8°	0°	8°	0°