

机芯科技
HUTCHIP

HCNR3908

30V N-Channel MOSFET

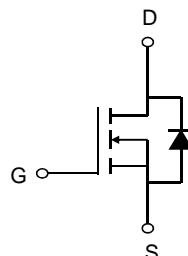
General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

Features

V_{DS}	30V
I_D (at $V_{GS}=10V$)	30A
$R_{DS(ON)}$ (at $V_{GS}=10V$)	6.0mΩ(Typ)
$R_{DS(ON)}$ (at $V_{GS}=4.5V$)	9.5mΩ(Typ)

100% UIS TESTED!
100% ΔV_{ds} TESTED!



Absolute Maximum Ratings $T_A=25^\circ C$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D (TC=25°C)	50	A
	I_D (TC=100°C)	30	A
Drain Current – Pulsed	I_{DM}	240	A
Single pulse avalanche energy	E_{AS}	100	mJ
Maximum Power Dissipation	P_D	35	W
Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	°C

Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance junction-case	$R_{\theta JC}$		3.6	°C /W
Thermal Resistance junction-to-Ambient	$R_{\theta JA}$		62	°C /W

Electrical Characteristics (TJ=25°C unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	30			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=30V, V_{GS}=0V$			1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$			± 100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.6	2.5	V
$R_{DS(ON)}$	Drain-Source On-State Resistance	$V_{GS}=10V, I_D=16A$		6.0	8.5	$m\Omega$
		$V_{GS}=4.5V, I_D=8A$		9.5	13.0	$m\Omega$
DYNAMIC PARAMETERS						
C_{lss}	Input Capacitance	$V_{DS}=25V, V_{GS}=0V, F=1.0MHz$		1270		pF
C_{oss}	Output Capacitance			132		pF
C_{rss}	Reverse Transfer Capacitance			110		pF
SWITCHING PARAMETERS						
$t_{d(on)}$	Turn-on Delay Time	$V_{GS}=10V$ $V_{DS}=15V$ $R_L=0.75\Omega$ $R_{GEN}=3\Omega$		12		nS
t_r	Turn-on Rise Time			18		nS
$t_{d(off)}$	Turn-Off Delay Time			27.6		nS
t_f	Turn-Off Fall Time			21		nS
Q_g	Total Gate Charge	$V_{DS}=15V, I_D=20A, V_{GS}=4.5V$		22		nC
Q_{gs}	Gate-Source Charge			4.6		nC
Q_{gd}	Gate-Drain Charge			3.3		nC
V_{SD}	Diode Forward Voltage	$V_{GS}=0V, I_{SD}=1A$			1.2	V
R_g	Gate resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$		2.5		Ω

Note:

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

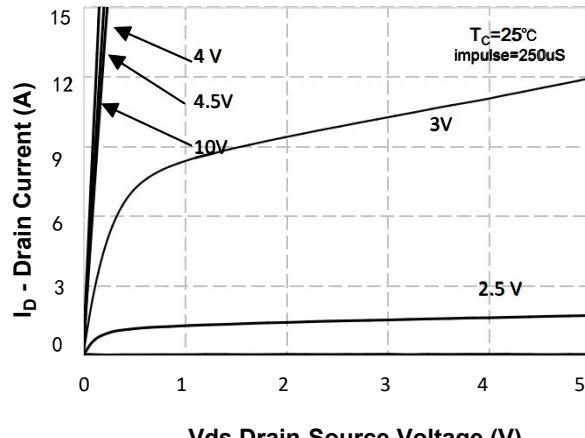


Figure 1. On-Region Characteristics

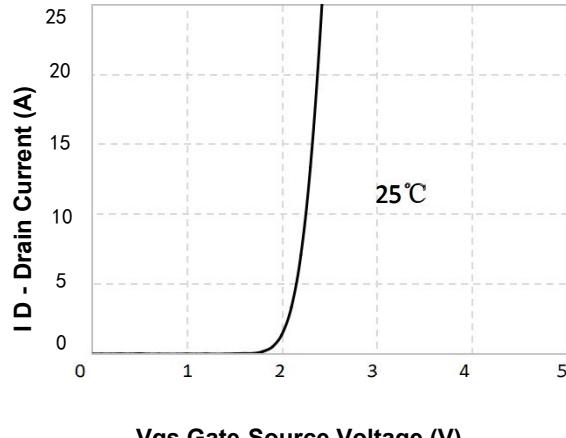


Figure 2. Transfer Characteristics

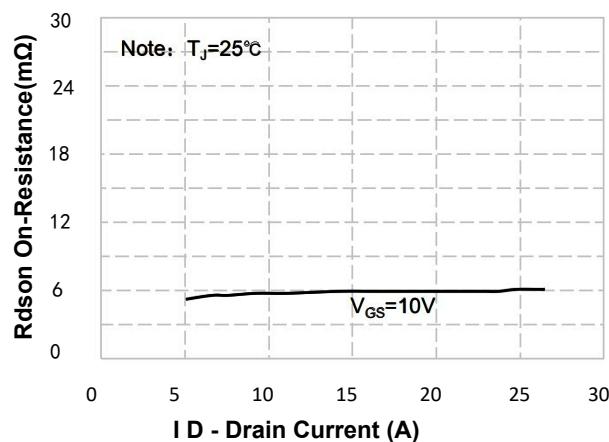


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

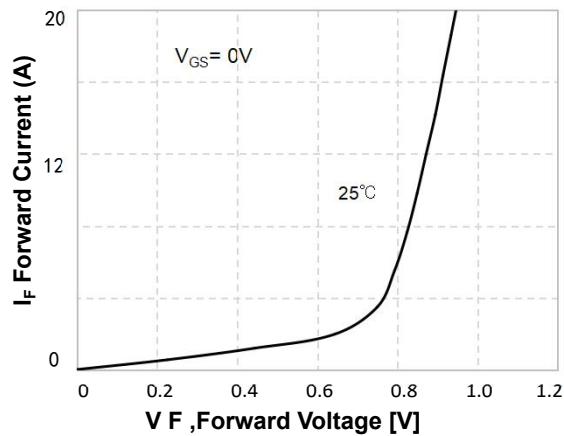


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

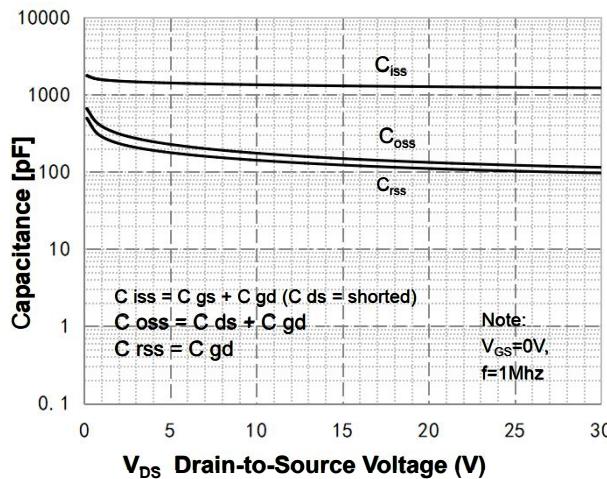


Figure 5. Capacitance Characteristics

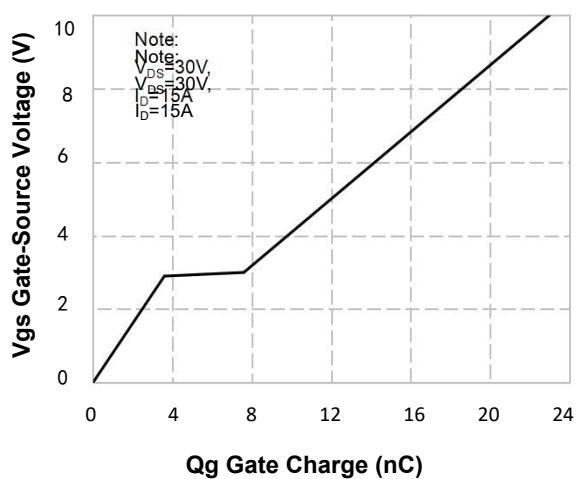


Figure 6. Gate Charge Characteristics

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

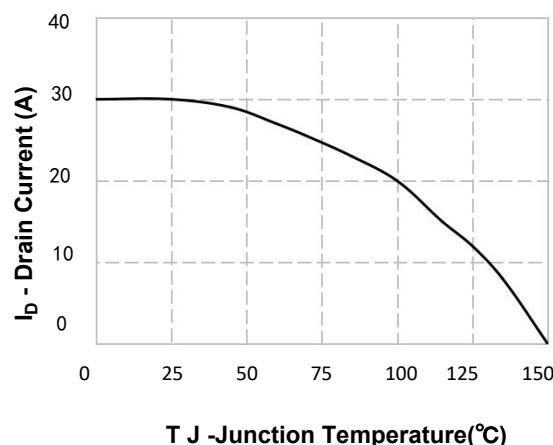


Figure 7. Maximum Continuous Drain Current vs Case Temperature

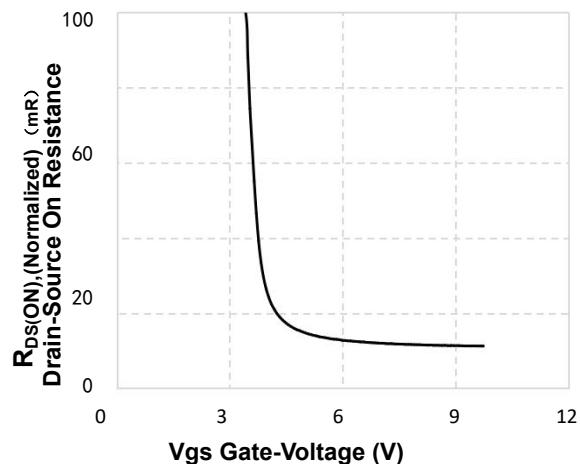


Figure 8. On-Resistance Variation vs Gate Voltage

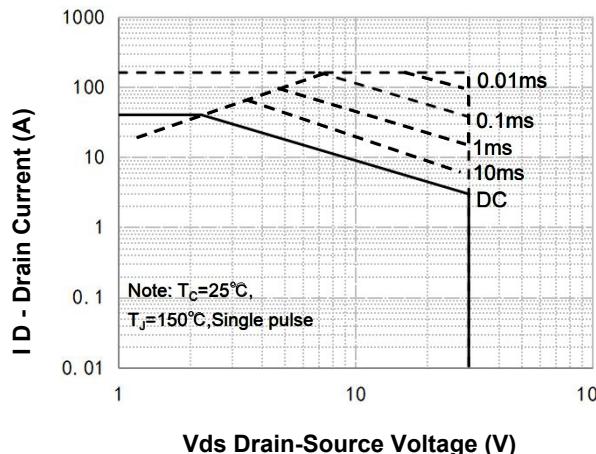


Figure 9. Maximum Safe Operating Area

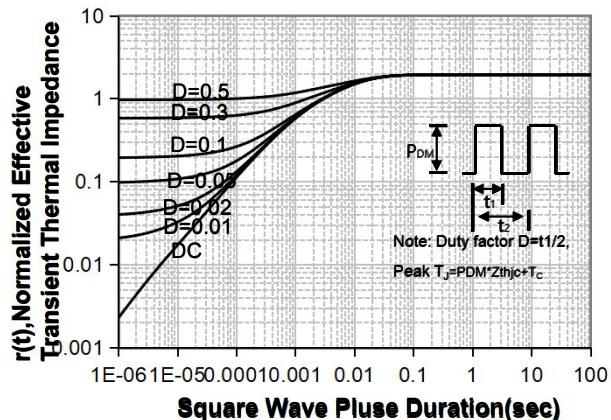


Figure 10. Transient Thermal Response Curve

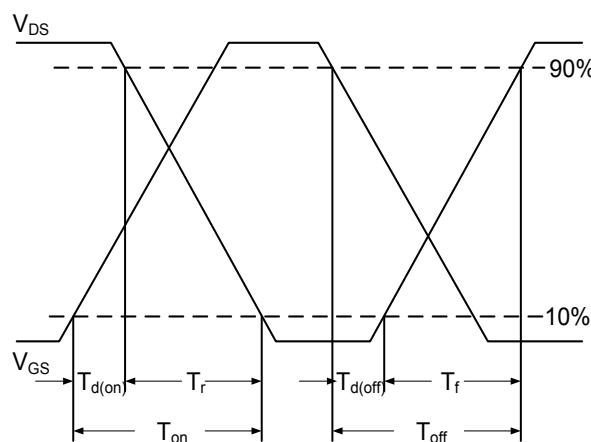


Fig.10 Switching Time Waveform

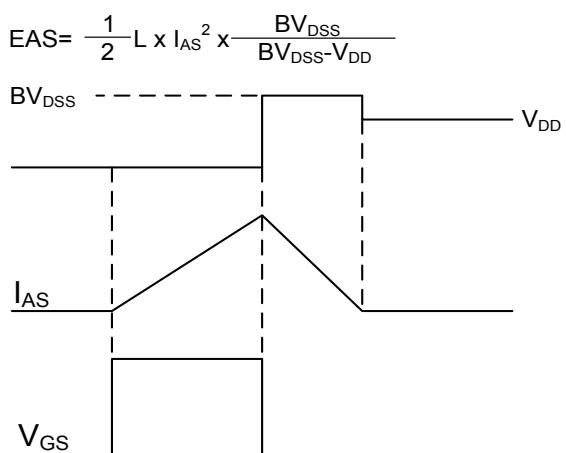
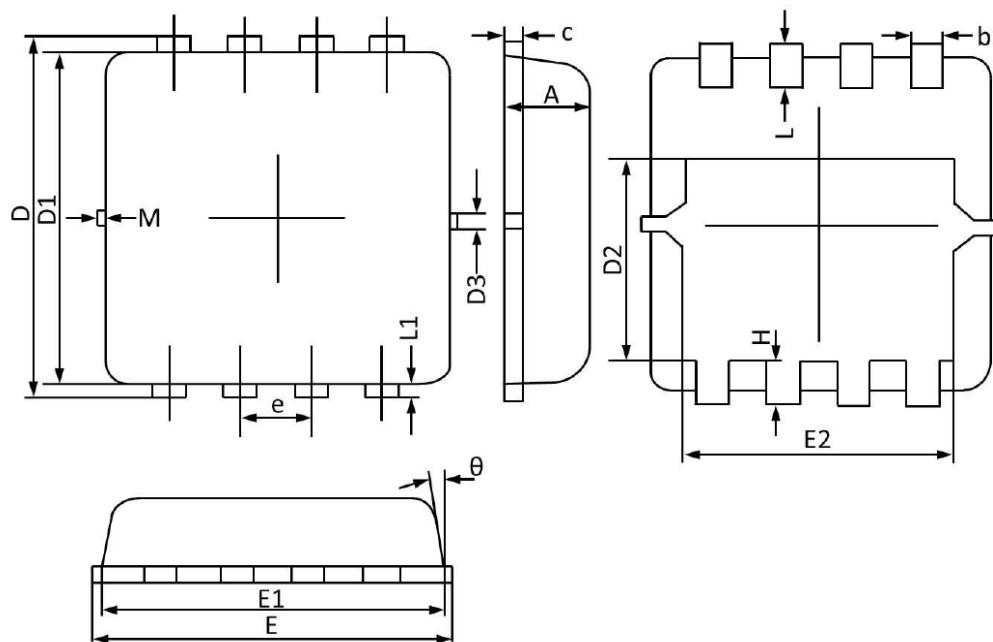


Fig.12 EAS Waveform

PDFN3x3 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	0.800	0.700	0.031	0.028
b	0.350	0.250	0.013	0.010
c	0.250	0.100	0.009	0.004
D	3.450	3.250	0.135	0.128
D1	3.200	3.000	0.125	0.119
D2	1.980	1.780	0.077	0.070
D3	0.130(REF)		0.005(REF)	
E	3.400	3.200	0.133	0.126
E1	3.200	3.000	0.125	0.119
E2	2.590	2.390	0.102	0.094
e	0.650(BSC)		0.026(BSC)	
H	0.500	0.300	0.019	0.011
L	0.500	0.300	0.019	0.011
L1	0.130(REF)		0.005(REF)	
θ	12°	0°	12°	0°
M	0.150(REF)		0.006(REF)	